

ABSTRACT

A thin film transistor array plate, a liquid crystal display panel and a method of preventing electrostatic discharge thereof are provided. The thin film transistor array comprises a substrate, pixel structures, switching devices, lead lines and electrostatic discharge (ESD) protecting circuits. The substrate has a display region and a peripheral circuit region. The pixel structures are disposed within the display region and the switching devices are disposed within the peripheral circuit region. The lead lines are disposed on the substrate to electrically connect the pixel structures and the switching devices. The ESD protecting circuits are disposed within the peripheral circuit region such that each ESD protecting circuit is electrically connected to the corresponding switching devices. The ESD protecting circuits prevent electrostatic discharge, simplify the circuit layout and improve production efficiency of the plate and the panel.